

ABSTRACT OF THE DISCLOSURE

A wireless Ethernet network device has active and low power modes. A first voltage regulator regulates supply voltage during the active mode. A second voltage regulator dissipates less power than the first voltage regulator and regulates supply voltage during the low power mode. A medium access controller (MAC) device selects the first voltage regulator during the active mode and the second voltage regulator during the low power mode. A crystal oscillator outputs a timing signal to the first PLL during the active mode. A first oscillator selectively generates a first clock signal during the low power mode. The first oscillator dissipates less power than the crystal oscillator.